i-SYST

Your Smart Machine
Solution Partner

TECHNICAL SUPPORT

Free technical support is provided via email to support@i-syst.com and indirectly through community forums and groups :

http://ez.analog.com
http://www.dsprelated.com

OPERATING SYSTEMS

The Blackfin® processors are widely supported by many RTOS/RTKernel vendors and the Open Source community.
Commercial : *Fusion RTOS, INTEGRITY, Nucleus Plus, OSEck, RTA-OSEK, RTXC Quadros, ThreadX, VelOSity, µ-velOSity, µC/OS-II.*
Free : *uClinux, RTEMS, VDK*

The IBF-DSP561 BSP for u-boot & uClinux are provided, support for other RTOS are available through our customization services

CUSTOMIZATION

I-SYST can assist you in both software and hardware development: Custom base board, BSP customization, Firmware development, etc...

For more information please visit us at: http://www.i-syst.com

IBF-DSP561 - Micromodule
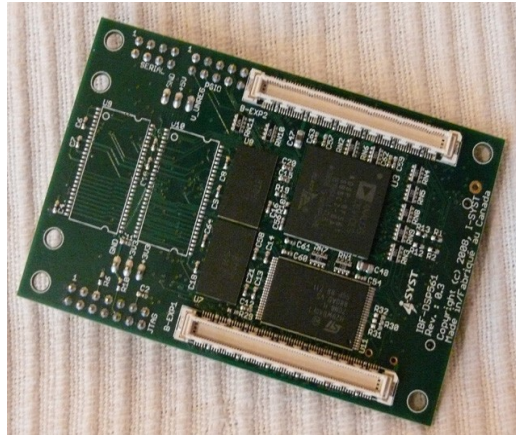
* Blackfin is a registered trademark of Analog Devices, Inc.

The Blackfin® Processors is a high performance 16-/32-bit embedded processor core with a 10-stage RISC MCU/DSP pipeline, variable length ISA for optimal code density, and full SIMD support with instructions for accelerated video and multimedia processing. Harvesting the Blackfin® ADSP-BF561 embedded dual core processor technology, the credit card size IBF-DSP561 micromodule is designed to provide highest performance and power efficiency for multi-format audio, video, voice and image processing; multi-mode baseband and packet processing; and real-time security and control processing. Full expansion capability of the ADSP-BF561 is achieved via the two low profile high density high speed Hirose connectors.   The on board Jtag allows to start software development immediately without needing any base board by simply providing a single 3.3v supply to the IBF-DSP561 via header pins or by optional plugin ISUP3508 supply board.

Features

- Dual Core Blackfin® ADSP-BF561 @ 600MHz per core.
- Up to 128MB 32bits 133MHz SDRAM.
- 8MB Flash
- 1- RS232
- 1- PPI Connector mapped to PPI0
- Dual low profile high density high speed Hirose 120 pins expansion connectors for direct access to all on chip peripherals
- Onboard core voltage regulator for single 3.3v supply operation
- Jtag interface
- 8 pins header direct 3.3v I/O via Processor Flags with pullups
- 4 - User LEDs
- Dimension : 56 x 85 mm
- Power Consumption (full speed with 128MB SDRAM) : ~2 W (3.3V @ 600mA)
- uClinux support

Applications

* Digital Still Cameras
* Digital Video Cameras
* Portable Media Players
* Digital Video Recorders
* Industrial and Instrumentation
* Set Top Boxes
* Consumer Multimedia
* Automotive Vision Systems
* Broadband Wireless Systems